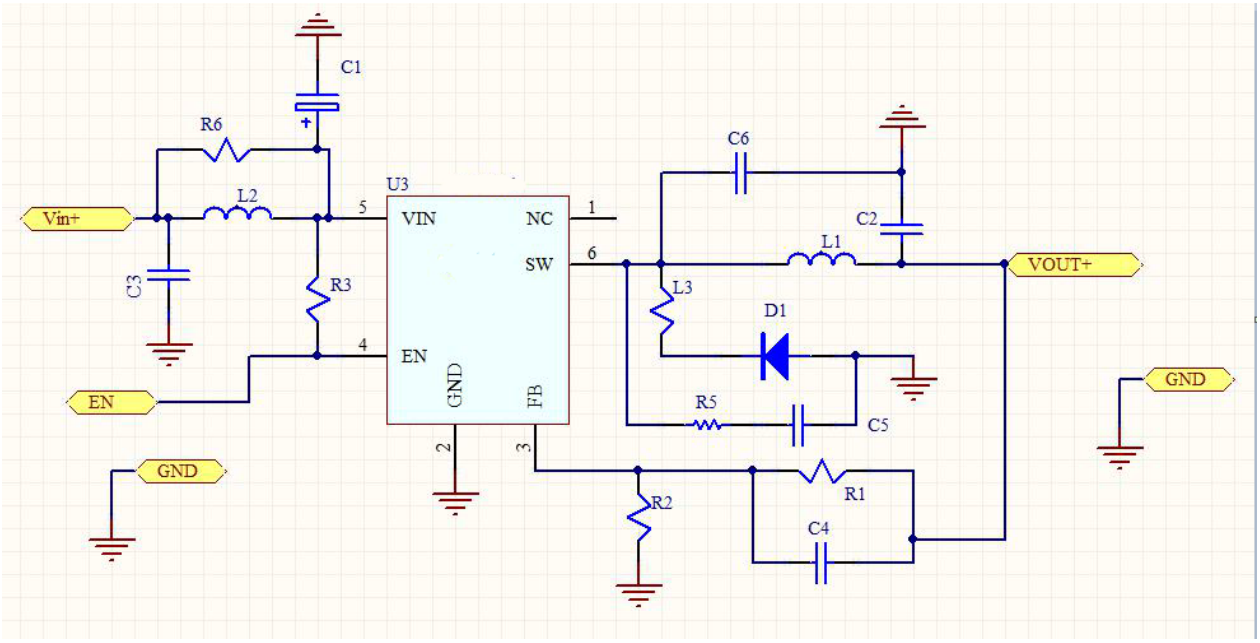
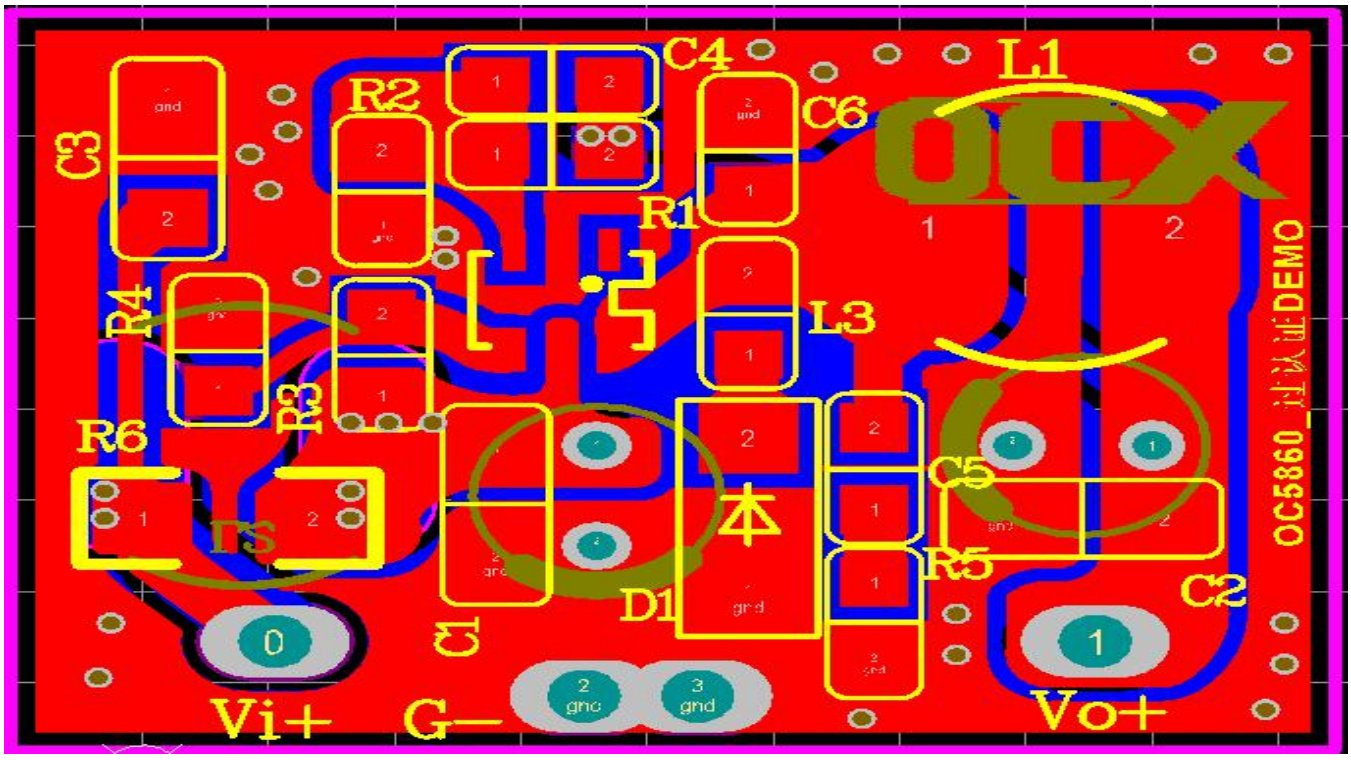
# 过传导辐射参考资料

## 1.典型应用原理图 Vi=12-48V Vo=5V Io=0.6A




## 2.PCB 图 Vi=12-48V Vo=5V Io=0.6A



# 3. BOM 清单：Vi=12-48V　Vo=5V　Io=0.6A

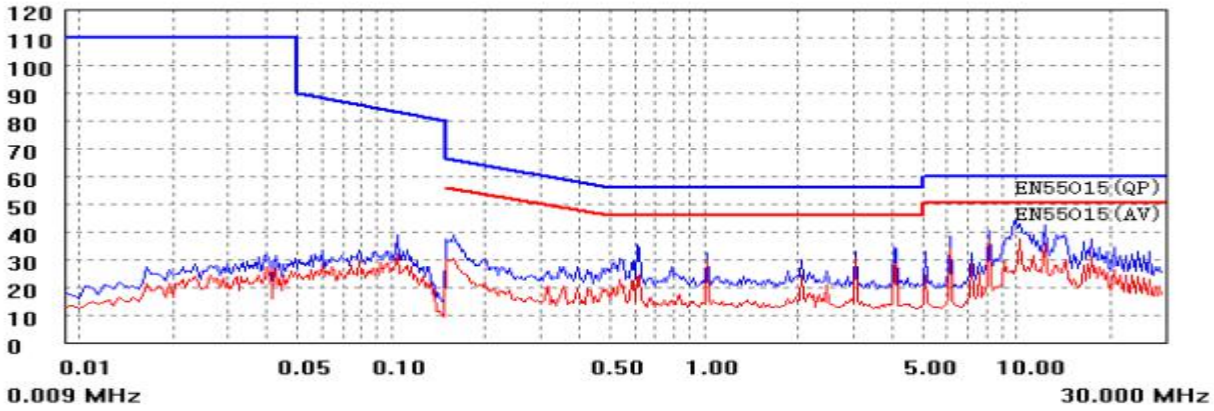
| 序号 | 材料名称 | PCB-Layout 位号 | 技术规格 | 标准用量 | 价格 |
|---|---|---|---|---|---|
| 1 | 贴片电阻 | R3 | 510K±5%　0805 | 1 | |
| 2 | 贴片电阻 | R1 | 120K±5%　0805 | 1 | |
| 3 | 贴片电阻 | R2 | 23.7K±5%　0805 | 1 | |
| 4 | 贴片电容 | C4 | 47pF±10%/25V/X7R/0805 | 1 | |
| 5 | 贴片电容 | C5 | 1nF±10%/25V/X7R/0805 | 1 | |
| 6 | 贴片电容 | C6 | 470pF±10%/25V/X7R/0805 | 1 | |
| 7 | 贴片电容 | C3 | 4.7uF±10%/50V/X7R/1206 | 1 | |
| 8 | 贴片电容 | C2 | 22uF±10%/25V/X7R/1206 | 1 | |
| 9 | 插件电解 | C1 | 22uF/100V | 1 | |
| 10 | 电感 | L1 L2 | FNR6045-100uH | 2 | 需要屏蔽电感 |
| 11 | 二极管 | D1 | SS260/60V/2A/SMA | 1 | |
| 12 | 贴片磁珠 | L3 | 磁珠阻抗1000ohm/0805/0.8A | 1 | |
| 13 | IC | U1 | OC5860 | 1 | |

# 4. EMI 测试数据

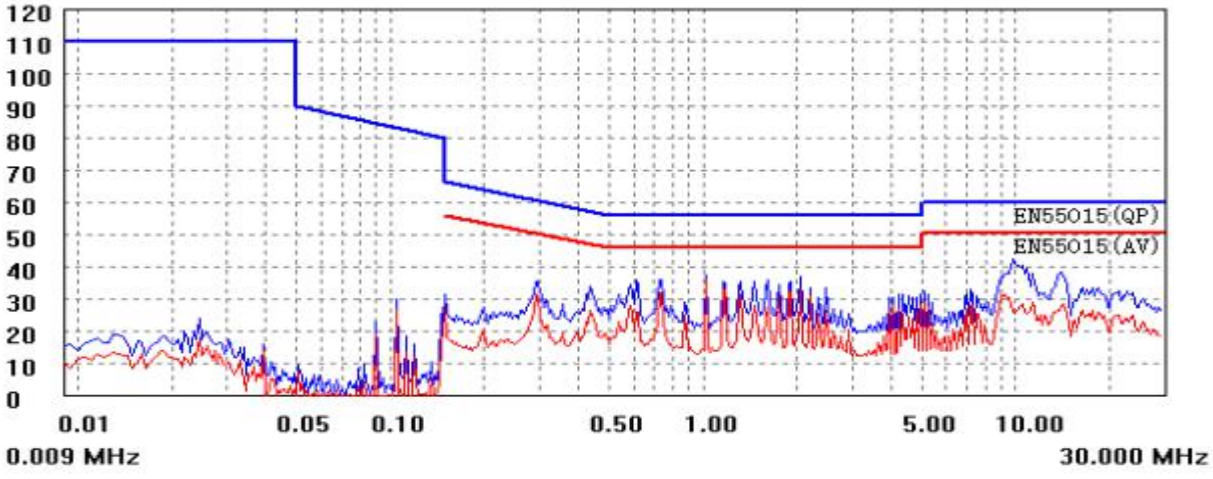
## Vi=12V　Vo=5V　Io=50mA


## Vi=12V　Vo=5V　Io=100mA

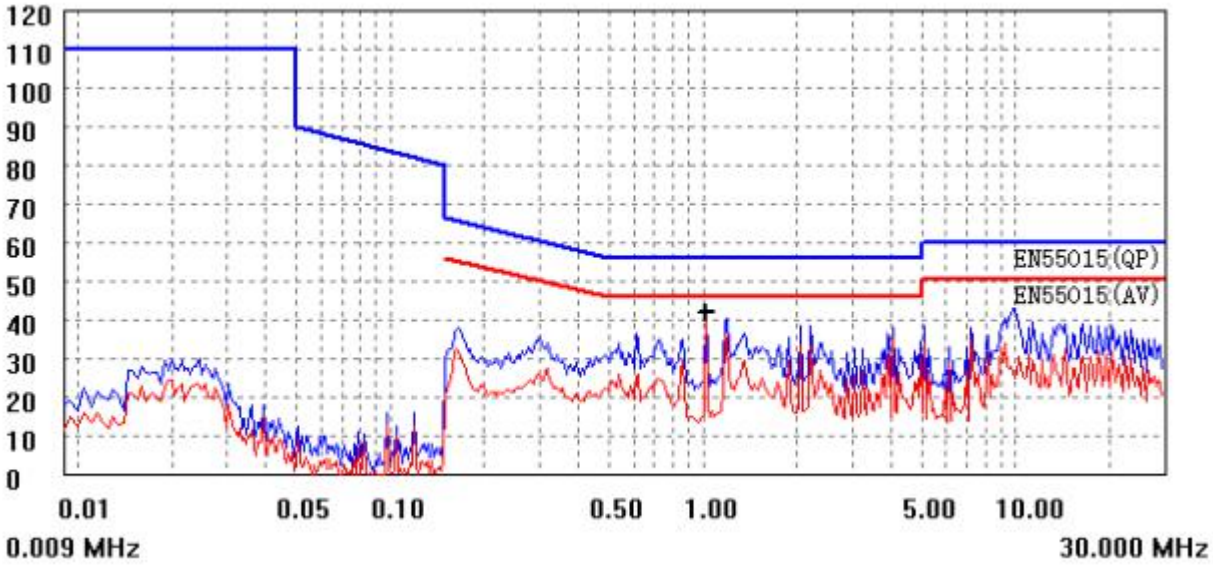

**Vi=12V　Vo=5V　Io=0.5A**



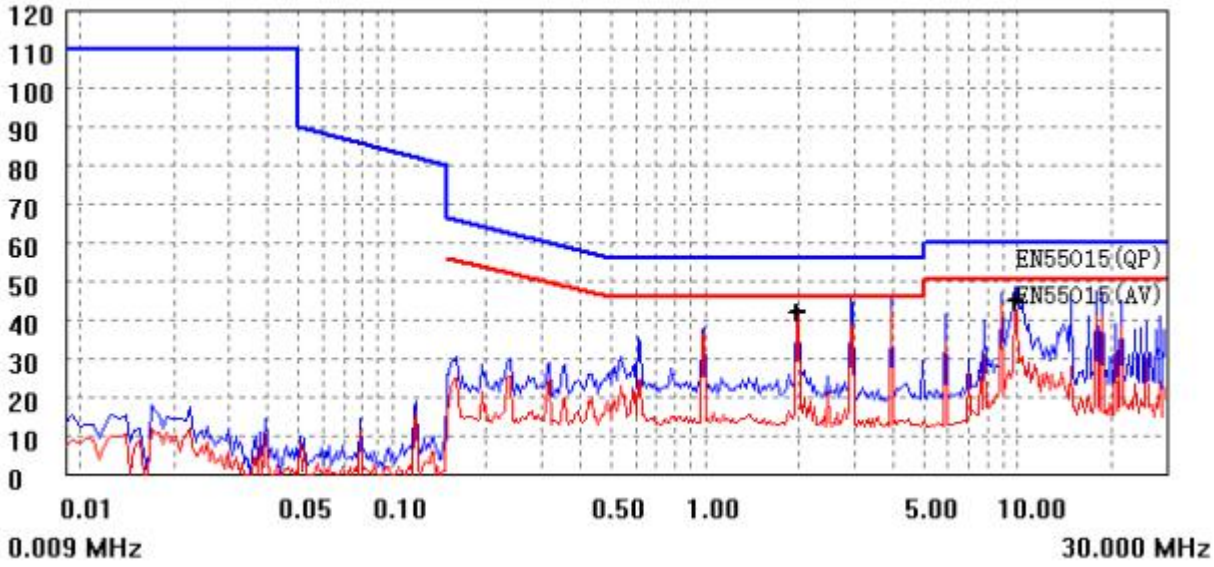

**Vi=48V　Vo=5V　Io=50mA**



## Vi=48V Vo=5V Io=100mA

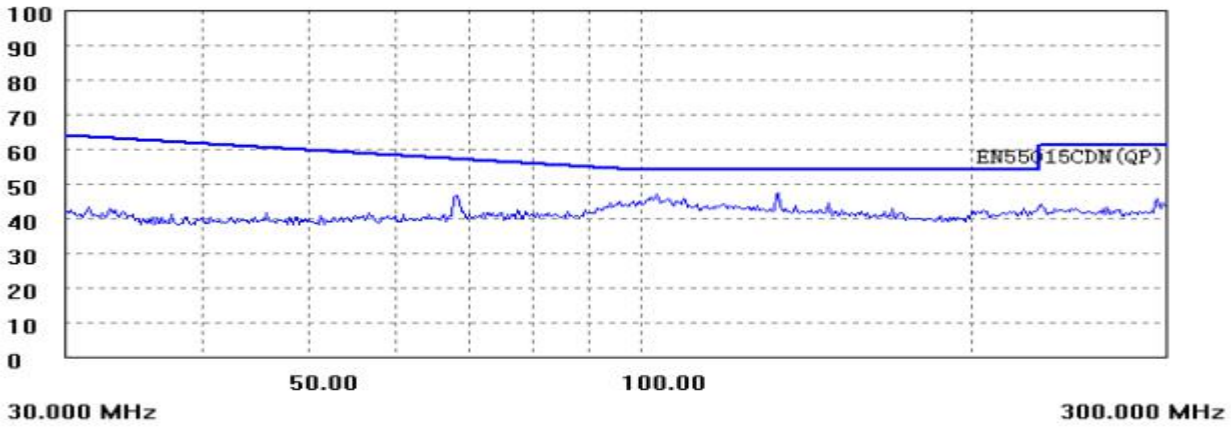



## Vi=48V Vo=5V Io=0.5A

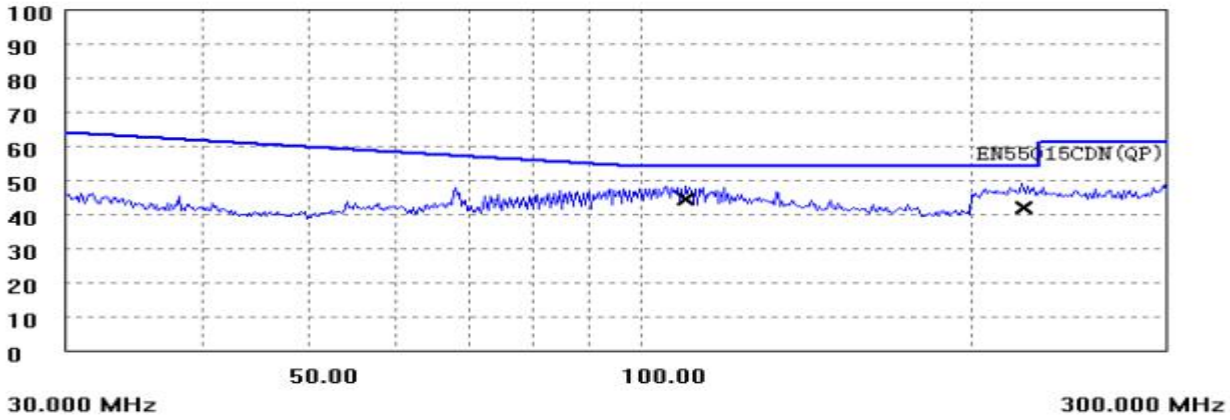


# 5.EMC 测试数据

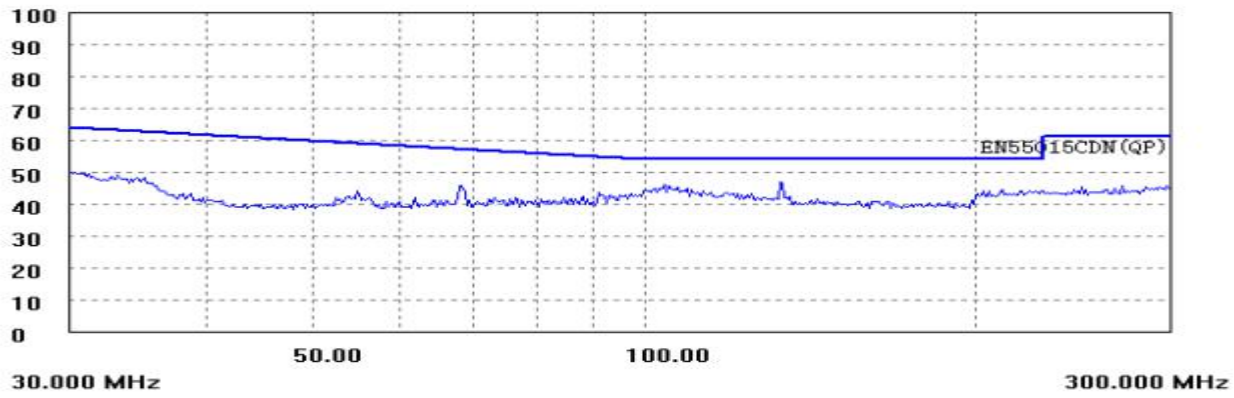
## Vi=12V Vo=5V Io=50mA



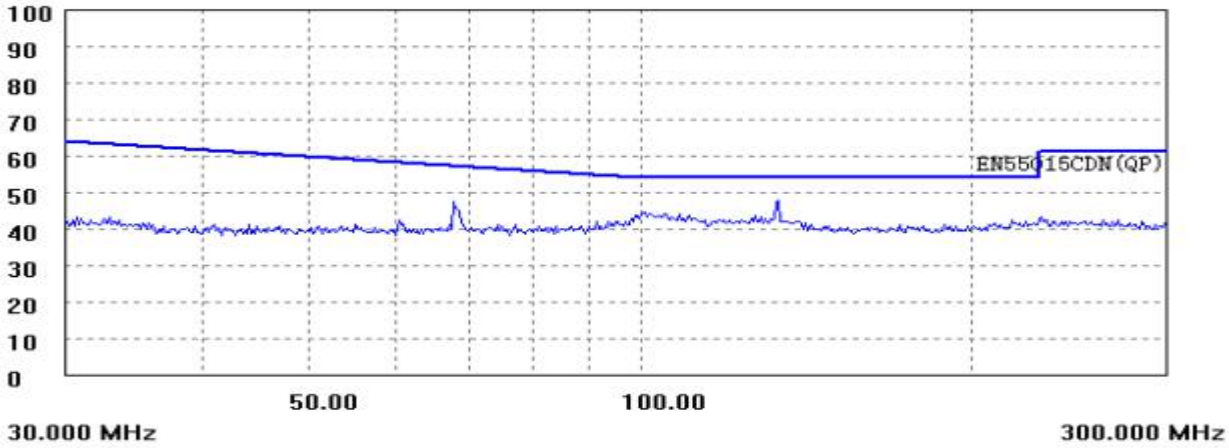

## Vi=12V Vo=5V Io=100mA



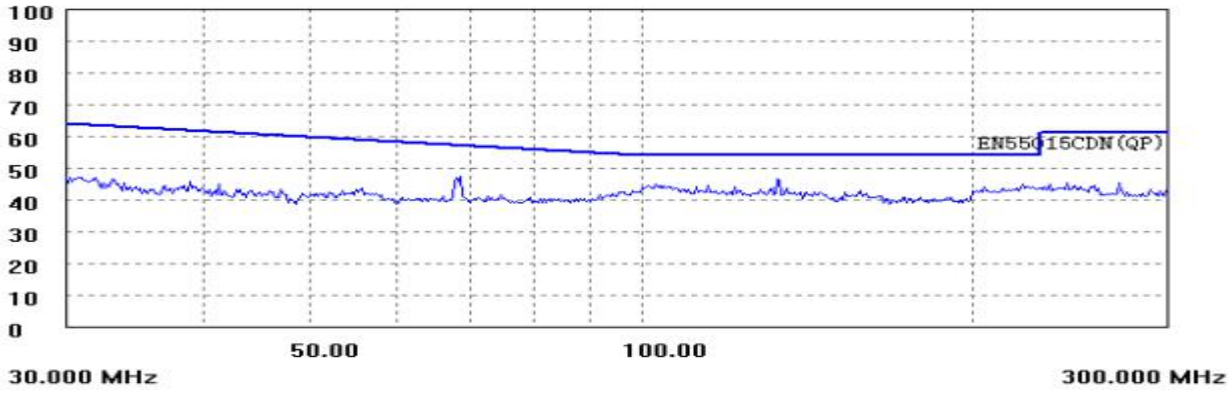

## Vi=12V Vo=5V Io=0.5A



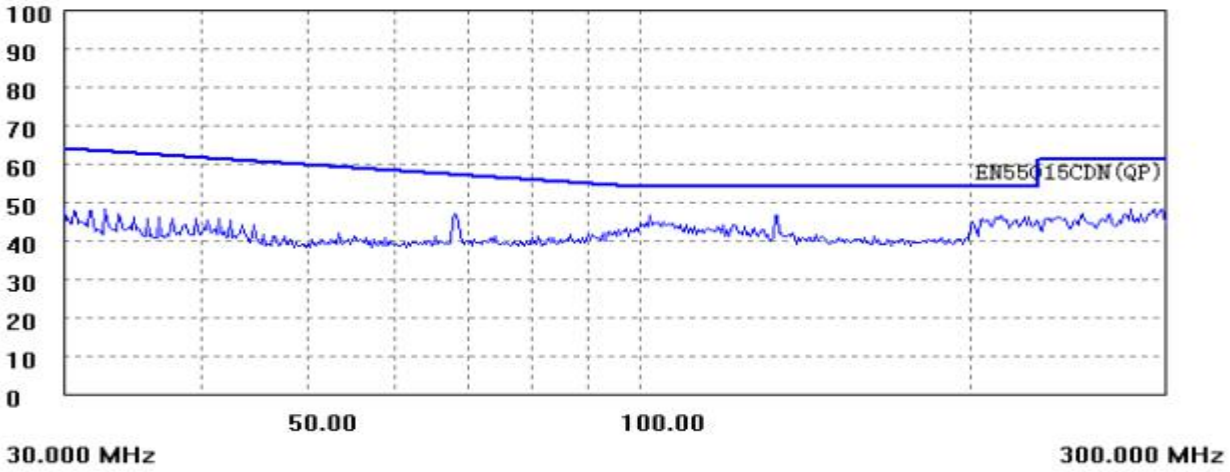
## Vi=48V Vo=5V Io=50mA




## Vi=48V Vo=5V Io=100mA

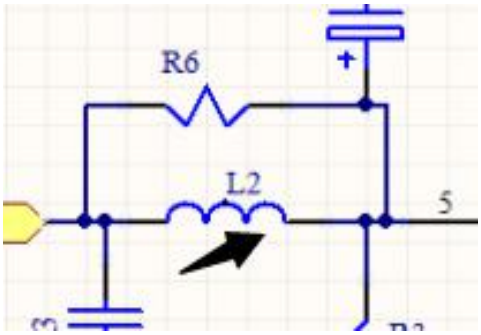



## Vi=48V Vo=5V Io=0.5A

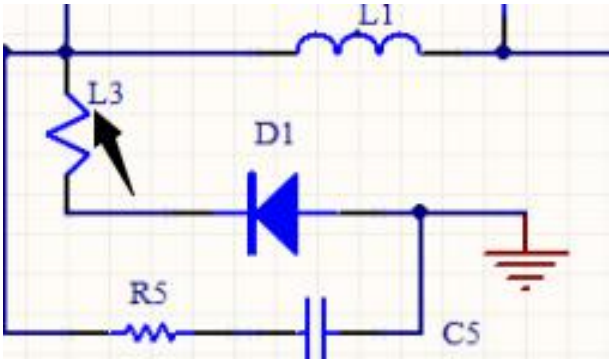


# 6.过传导辐射注意事项。

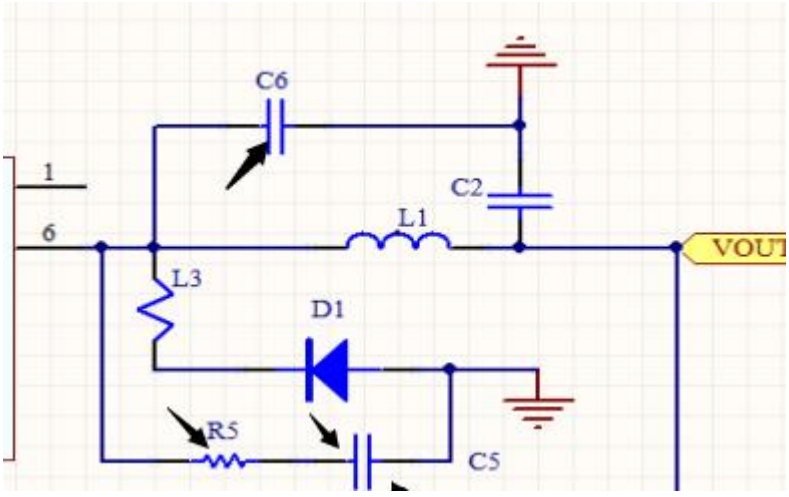
1. 输入端要加差模电感，电感量不要低于 33uH。




2. 续流二极管负极串磁珠，磁珠阻抗为 500R-1000R，最好是 1000R。




3. 磁珠加电容吸收，续流二极管加 RC 吸收，R=10-20R，C=1-2.2nF。




4.输入差模电感和功率电感用屏蔽型电感。

5.在 Layout 时要注意控制大电流环路，环路越小越容易过传导辐射。